

AresONE 1600GE

Validating 1.6T Networks and AI Data Centers

The rapid progress of AI can put significant pressure on data centers, highlighting the importance of efficient scaling and optimization.

With the increased diversity and scale of AI and data centers, traditional testing methods cannot accurately predict and measure the reliability of today's complex production networks. Traditional methods, while effective, often struggle to keep up with the rapid advancements and increasing complexity of modern AI and data center networks. Automated and AI-driven testing solutions are becoming more prevalent in addressing these challenges.

Deploying 800GE and 1.6T Ethernet solutions helps mitigate infrastructure bottlenecks. Validating high-speed AI fabrics and realistically emulating complex AI workloads with advanced test tools is essential for ensuring reliable performance.

Future-Proofing AI and HSC Data Centers with Advanced Testing

Scalable infrastructure is crucial for AI data centers to manage the increasing demands of AI workloads and evolving technology. Scaling AI data centers demand more than component-level validation. Validating AI fabrics, GPUs, AI NICs, and AI accelerators requires real-world network conditions. Test strategies must incorporate both physical-layer testing and full-stack AI workload emulators to optimize system-level performance.

By focusing on scalable infrastructure, AI data centers can achieve higher efficiency, reliability, and performance, supporting the rapid advancement of AI technologies.

The Solution



AresONE 1600GE is a new 1.6 Terabit Ethernet (1.6T) hardware test platform for traffic generation and analysis. It provides AI fabric, AI devices and high-speed devices validation with emulated GPU clusters at high speed.

Running Keysight AI Data Center Builder software on AresONE 1600GE enables the Collective Benchmarks application to evaluate AI cluster communication performance over 224G SerDes and assess network impact on job completion time, performance isolation, load balancing, and congestion control for optimized AI training performance.

The Keysight AresONE 1600GE is designed to validate AI infrastructure, AI network components, and test Scale-Out traffic and every data path in the system, while adding real-life impairments at speeds from 200GE to 1600GE.

Keysight AresONE 1600GE and IxNetwork software enables validation of high-speed devices from Layer 1 (AN/LT, FEC, BERT) to upper layer routing protocols, UEC or ROCEv2 implementations.

Deploy a Versatile High-Speed Ethernet Platform to Increase Test Plan Coverage

- Select an IHS or RHS optic chassis. Both offer four OSFP1600 front panel ports over 2U
- Support 1x1600GE, 2x800GE, 4x400GE, and 8x200GE configurations.
- Test reliability and performance of Ethernet devices that use 212 Gb/s electrical lane interfaces, including GPUs, AI NICs, AI accelerators, ToR switches or high-speed routers

Drive Faster Innovation

- Speed the AI fabric and data center infrastructure validation process, to ensure faster deployment and improved reliability of network equipment and AI fabric.
- Accelerate testing of high-speed devices supporting AI and high-speed computing data centers through end-to-end automation that enables protocol, performance and deployment assessment.

Optimize Performance at All Layers

- Offers Keysight's traditional Layer 1 Bit Error Rate Testing (BERT).
- Provides additional lower layer test capabilities for the Physical Coding Sublayer (PCS), Forward Error Correction (FEC), and Frame Loss Ratio (FLR) validations, ensuring devices are ready for production network deployment.
- Addresses lab test beds for Layer 2 and Layer 3 RFC benchmarks, network, and protocol emulation for scale and performance validation.
- Assesses next-generation routers capabilities against the 1.6T backbone traffic requirements
- Tests LLR and CBFC implementations of UEC in devices or networks or validate ROCEv2/ DCQCN implementation using mixed traffic flows.
- Validates AI fabric deployment with real AI workloads and industry validated CCL.

Highlights

- Layer 1 to Layer 3 IP testing from 200GE to 1.6TE PAM4 with 212 Gb/s and electrical lane signaling in a single test system.
- Benchmark your AI fabric with high scale tests enabled by Keysight AI Data Center Builder (KAI DCB) software
- Complete Layer 2 and Layer 3 RFC benchmark, network, and protocol emulation scale and performance testing with the IxNetwork software application with Rest API and RestPy test automation support.
- Support for up to 40 watts per port for high power consumption coherent optical transceivers.
- Compatible with IEEE P802.3dj 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s specifications.

Specifications

Part Numbers	941-0130	941-0131
Product Description	AresONE 1600GE 4-Port Rackmount Chassis with Native OSFP1600 Front Panel Ports, Integrated Heatsink (IHS) Optics	AresONE 1600GE 4-Port Rackmount Chassis with Native OSFP1600 Front Panel Ports, Riding Heatsink (RHS) Optics
Hardware Fixed Chassis System Specifications		
Number of ports	4-port OSFP1600 chassis	
Physical interfaces	Native OSFP1600 IHS physical front panel pluggable ports	Native OSFP1600 RHS physical front panel pluggable ports
Supported per port speeds	Default speeds included with the chassis: <ul style="list-style-type: none"> • 1x1600GE, 2x800GE, 4x400GE, and 8x200GE per port, PAM4 over 212 Gb/s electrical lanes • Optical transceiver and fiber cable interconnect support for all speeds • Active Electrical Cable (AEC) support for all speeds • (Planned) 1x800GE, 2x400GE, 4x200GE, and 8x100GE per port, PAM4 over 106 Gb/s electrical lanes 	
Number of users	4 users per chassis (1 user per front panel physical port ownership)	
Interface protocols specifications for 1600GE/212 Gb/s electrical lane support	IEEE P802.3dj Media Access Control Parameters for 1.6 Tb/s and Physical Layers and Management Parameters for 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Operation	
Layer 1 support 1600GE PAM4 speeds for 212 Gb/s electrical lanes	<ul style="list-style-type: none"> • PAM4, 1600/800/400/200GE speeds • KP4 (RS-544, 514) Ethernet Forward Error Correction, IEEE 802.3 Clause 119 • FEC Correctable and uncorrectable statistics per-port • FEC symbol error injection for all speeds • FEC Codeword symbol error correction distribution statistics • Pre-FEC BER and Frame Lose Ratio (FLR) measurements • PCS Rx per lane and port statistics • Layer 1 BERT with PRBS-7Q, PRBS-9Q, PRBS-11Q, PRBS-13Q, PRBS-15Q, PRBS-23Q, PRBS-31Q and SSPRQ (Tx only) pattern generation support and Rx-side statistics and analysis. Additional test, pattern controls and pattern detection are included. 	
OSFP1600 optical transceiver support	<ul style="list-style-type: none"> • Support for OSFP1600 specification compliant optical transceivers up to 40 watts of consumption 	<ul style="list-style-type: none"> • Support for OSFP1600 specification compliant optical transceivers up to 30 watts of consumption
OSFP1600 Active Electrical Cable support	<ul style="list-style-type: none"> • Active Electrical Cable (AEC) support; please consult the factory for specific support information 	
(Upgrade Path) OSFP1600, passive copper cable support	<ul style="list-style-type: none"> • OSFP1600 passive copper cable support for up to 1.0 meters in length. For first generation the upgrade is available via return to factory using Part Numbers 942-2130 (IHS) or 942-2131 (RHS). • Auto-negotiation and Link Training support for passive copper direct attached cables (DAC) for all supported Ethernet speeds per port • Consult the factory for support of passive copper cable lengths that are longer than those stated above 	
Common Management Interface Specification (CMIS)	<ul style="list-style-type: none"> • Support for the CMIS 4.0 and 5.x specifications including read/write access to all CMIS pages and registers • Support for C-CMIS 1.0 (Coherent CMIS) • (Planned) Support for Application Selection Code (AppSel) feature of CMIS with auto-detection and transceiver configuration support • CMIS will operate with optical and copper interconnect media to the extent they are supported by the interconnect manufacturer 	

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	<ul style="list-style-type: none"> CMIS is exposed through the IxExplorer application and Tcl test automation support 	
Digital Optical Monitoring (DOM)	Automatically provides information from the interconnect device plugged into the test port, along with the device status, electrical power, temperatures, power class, laser power and various LOL and LOS threshold and alarm monitoring information. The DOM also provides feedback when alarms and thresholds are exceeded. This capability is provided with the IxExplorer and IxNetwork applications.	
1600ZR Coherent Optics Transceiver support	<ul style="list-style-type: none"> CMIS 5.x and C-CMIS 1.0 (Coherent CMIS) provide Read/Write access to all management pages and Versatile Diagnostics Monitoring (VDM) registers via IxExplorer GUI and Tcl test automation programming interface (Planned) Support for Application Selection Code (AppSel) feature of CMIS with auto-detection and configuration support The IxExplorer application supports the Digital Optical Monitoring (DOM) feature that automatically monitors and reports the module case temperature, temperature warning limits, and the maximum temperature threshold limits. It is highly recommended that the DOM feature be active to monitor the temperature behavior of installed high power consumption optical transceivers before conducting long duration tests. 	
Fixed chassis system dimensions	<ul style="list-style-type: none"> 30.0" (L) x 17.32" (W) x 3.46" (H) 762 mm (L) x 440 mm (W) x 88 mm (H) 	
Fixed chassis system weights	Hardware only: 59.4 lbs. (27.0 kg)	
Fixed chassis system electrical power	200-240V, 50/60Hz, 10A	
Temperature (ambient air)	<ul style="list-style-type: none"> Operating: 41°F to 104°F (5 °C to 40 °C) Storage: -4°F to 122°F (-20 °C to 50 °C) 	<ul style="list-style-type: none"> Operating: 41°F to 86°F (5 °C to 30 °C) Storage: -4°F to 122°F (-20 °C to 50 °C)
Humidity (ambient air)	<ul style="list-style-type: none"> Operating: 5 % to 85 % RH, non-condensing Storage: 5 % to 85 % RH, non-condensing 	
Safety	<ul style="list-style-type: none"> IEC 62368-1 EN IEC 62368-1+A11 UL 62368-1 / CSA C22.2 No. 62368-1 	
Emissions and immunity	<ul style="list-style-type: none"> FCC Part 15B, Class A ICES-003(A)/NMB-003(A) EN 55032 Class A, EN 55035, EN 61000-3-2, EN 61000-3-3 AS/NZS CISPR 32, Class A KS C 9832 Class A, KS C 9835 VCCI – CISPR 32, Class A 	
Regulatory approvals	<ul style="list-style-type: none"> UL (USA, Canada) CE (European Union) UKCA (United Kingdom) RCM (Australia, New Zealand) KCC (S Korea) VCCI (Japan) 	
Environmental	<ul style="list-style-type: none"> RoHS Directive 2011/65/EU, Directive (EU) 2015/863 WEEE Directive 2012/19/EU China RoHS 	
Chassis Synchronization Extensibility		
Maximum number of chassis in single test topology	<p>The Metronome Timing System (942-0090) is used for synchronizing a total of eight or more chassis at one time.</p> <p>Sub nano-second time sync through PTP dedicated port enables large number of ports in the same time domain.</p>	

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Transmit Feature Specifications		
Transmit engine	Wire-speed packet generation with timestamps, sequence numbers, data integrity, and packet group signatures.	
Max. streams per port and 1600GE PAM4 speeds	<ul style="list-style-type: none"> • 1x1600GE: 64 (per FPP) • 2x800GE: 64 (per fan-out) • 4x400GE: 64 (per fan-out) • 8x200GE: 64 (per fan-out) 	
Stream controls	<ul style="list-style-type: none"> • Rate and frame size change on the fly • Advanced stream scheduler • Sequential stream scheduler 	
Minimum frame size	1600GE, 800GE, 400GE and 200GE PAM4 speeds: <ul style="list-style-type: none"> • 64 bytes at full line rate • 61 bytes at less than full line rate (approximately 90% utilization) 	
Maximum frame size for 1600GE PAM4 speeds	14,000 bytes	
Maximum frame size in data center Ethernet	9,216 bytes for all speeds	
Priority flow control (4:1) for 1600GE PAM4 speeds	<ul style="list-style-type: none"> • 4 line-rate-capable queues, each supporting up to 9,216-byte frame lengths • 1 line-rate-capable queue, non-blocking supporting up to 9,216-byte frame length 	
Frame length controls	Fixed, increment by user-defined step, weighted pairs (up to 16K for 1600GE/800GE and up to 8K for lower speed), uniform, repeatable random, IMIX, and Quad Gaussian	
User-Defined Fields (UDF)	<ul style="list-style-type: none"> • Fixed, counter (increment or decrement by user-defined step), value list, and random configurations. • Up to 6 UDFs supporting fixed, nested counter, random configurations (32-bit or 2x16-bit wide), or value list configurations (64-bit, 2x32-bit, 4x16-bit, or 4x8-bit wide). • Up to 4 additional UDFs supporting fixed, counter, random configurations (32-bit, or 2x16-bit wide). • Split UDFs (for example, 2x16 configuration) support different byte offset per UDF segment. 	
Value lists (max.) per port for 1600GE PAM4 speeds	<ul style="list-style-type: none"> • 1x1600GE: 64K / port /UDF • 2x800GE: 64K / port /UDF • 4x400GE: 128K /2-port /UDF • 8x200GE: 128K / 4-ports /UDF 	
Sequence (max.) for 1600GE PAM4 speeds	<ul style="list-style-type: none"> • 1x1600GE: 16K / port /UDF • 2x800GE: 32K /port /UDF • 4x400GE:32K /port /UDF • 8x200GE: 32K / 2-ports /UDF 	
Error generation (FEC and standard Keysight L2/3 Ethernet in 1600GE PAM4 mode only)	<ul style="list-style-type: none"> • FEC symbol error-injection allows the user to inject FEC symbol errors using various weighted methods to achieve specific bit error rates (BER) • Generate good CRC or force bad CRC, undersize and oversize standard Ethernet frame lengths, and bad checksum 	
Hardware checksum generation	Checksum generation for IPv4, IP over IP, ICMP/GRE/TCP/UDP, L2TP, GTP, and multilayer checksum; support for protocol verification for control plane traffic	
Link fault signaling for all speeds	<ul style="list-style-type: none"> • Reports, no fault, remote fault, and local fault port statistics • Generate local and remote faults with controls for the number of faults and order of faults • Option to have the transmit port ignore link faults from a remote link partner and send traffic anyway 	
Latency measurement resolution	<ul style="list-style-type: none"> • 1600GE: 0.312 ns • 800GE and 400GE: 0.625 ns • 200GE: 1.25 ns 	

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Intrinsic latency compensation	Removes inherent latency error from the port electronics for all speeds	
PPM	(Planned) Ability to adjust the parts-per-million (ppm) line frequency: +/- 105 ppm on all the ports of the fixed chassis system for all speeds	
Receive Feature Specifications		
Receive engine	Wire-speed packet filtering, capturing, real-time latency, and inter-arrival time for each packet group, with data integrity, and sequence checking capability	
Trackable receive flows per port without Sequence checking and with Tx/Rx synch	<ul style="list-style-type: none"> • 1600GE: 16K full statistics • 800GE, 400G, 200GE: 32K full statistics 	
Trackable receive flows per port with and without Sequence checking and no Tx/RX synch	<ul style="list-style-type: none"> • 1600GE: 16K full statistics • 800GE, 400G, 200GE: 32K full statistics 	
Filters (user-defined statistics, UDS)	2 SA/DA pattern matchers, 2x16-byte user-definable patterns. 6 UDS counters are available with offsets for start of frame	
Hardware capture buffer	1 MB available per front panel OSFP1600 port and for the fan-out modes on that port	
Standard statistics and rates	Link state, line speed, frames sent, valid frames received, bytes sent/received, fragments, undersize, oversize, CRC errors, 6 user-defined stats, capture trigger (UDS 3), capture filter (UDS 4), data integrity frames, data integrity errors, sequence checking frames, sequence checking errors, ARP, and PING requests and replies	
FEC statistics for 1600GE PAM4 Ethernet speeds	1600GE PAM4 speeds: <ul style="list-style-type: none"> • FEC port statistics: Total Bit Errors, Max Symbol Errors, Corrected Codewords, Total Codewords, Uncorrectable Codewords, Frame Loss Ratio, Pre-FEC Bit Error Rate, and Codeword error distribution analysis. • FEC per lane Rx statistics: FEC Symbol Error Count, Corrected Bits Count, Symbol Error Rate, Corrected Bit Rate 	
Latency / jitter measurements	Cut-through, store and forward, forwarding delay, latency/jitter, MEF jitter, and inter-arrival time	
Receive-side PCS lanes port statistics counters for all speeds	PCS: Sync Errors, Illegal Codes, Remote Faults, Local Faults, Illegal Ordered Set, Illegal Idle, and Illegal SOF	
PCS receive-side statistics and indicators for 1600GE PAM4 Ethernet speeds	Per-lane PCS receive capabilities include: <ul style="list-style-type: none"> • Receive — per-lane PCS receive statistics, Physical Lane assignments, Lane Marker Lock, Lane Market Map, Relative Lane Skew, Lane Marker Error Count • Receive — per-lane FEC receive statistics; FEC Symbol Error Count, FEC Corrected Bits Count, FEC Symbol Error Rate, FEC Corrected Bit Rate 	

Application Support

OSFP1600GE Optics

OSFP1600-DR-IHS-XCVR	OSFP1600-DR-IHS-XCVR, OSFP1600-2xDR4, 2x800GE pluggable optical transceiver with Integrated Head Sink (IHS), SMF (single mode), 8x212Gb/s electrical lanes, Dual MPO-12 APC connector, 500m reach. This optical transceiver is compatible with AresONE 1600GE OSFP1600 with Integrated Head Sink (IHS) (941-0130), Interconnect and Network Performance Tester 1600GE 2-port benchtop (941-0095) and Interconnect and Network Performance Tester 1600GE 2-port half-rack chassis (941-0096). It supports 2x800GE PAM4 signaling output (948-0130)
OSFP1600-DR-RHS-XCVR	OSFP1600-DR-RHS-XCVR, OSFP1600-2xDR4, 2x800GE pluggable optical transceiver with Riding Head Sink (RHS), SMF (single mode), 8x212Gb/s electrical lanes, Dual MPO-12 APC connector, 500m reach. This optical transceiver is ONLY compatible with AresONE 1600GE OSFP1600 with Riding Head Sink (RHS) chassis (941-0131). It supports 2x800GE PAM4 signaling output (948-0131)
OSFP1600-DR4-CBL	OSFP1600-DR-CBL, MPO12 Single Mode Female to Female, Type B fiber optic cable, 2-meters length. (942-3130). Compatible with OSFP1600-DR-IHS-XCVR (948-0130) and OSFP1600-DR-RHS-XCVR (948-0131) dual connector optical transceivers (942-3130)

AresONE 1600GE 4-Port Rackmount Chassis

IxExplorer: Layer 1-3 wire-speed traffic generation, capture, and analysis with Layer 1 BERT, Forward Error Correction analysis and error injection with statistics, PCS Lanes Tx/Rx with statistics and reporting capability. IxTcl API for custom user script development for Layer 1-3 testing using the IxExplorer features

IxNetwork: Wire-rate traffic generation with service modeling that builds realistic, dynamically controllable data-plane traffic. IxNetwork offers the industry's best test solution for functional and performance testing by using comprehensive emulation for routing, switching, MPLS, IP multicast, broadband, authentication, Carrier Ethernet, and data center Ethernet protocols. Included with IxNetwork are test automation tools based on TCL, Python, and the Rest/RestPy.

Keysight AI Data Center Builder: The KAI Data Center Builder simplifies performance evaluation with KAI Collective Benchmarks app coupled with pre-packaged test methodologies and high-fidelity instruments, enabling AI operators to optimize infrastructure design and network performance. KAI Workload Emulation enables AI infrastructure teams to replicate real AI training behavior without deploying large GPU clusters, reducing costs while maintaining realism.

Ordering Information

Part Number	Description
AresONE 1600GE 4-Port Rackmount Chassis	
941-0130	AresONE 1600GE 4-port rackmount chassis with native OSFP1600 front panel ports, Integrated Heatsink (IHS) optics and L1-3 testing support. Includes all 1x1600GE, 2x800GE, 4x400GE, and 8x200GE Ethernet speeds
941-0131	AresONE 1600GE 4-port rackmount chassis with native OSFP1600 front panel ports, Riding Heatsink (RHS) optics and L1-3 testing support. Includes all 1x1600GE, 2x800GE, 4x400GE, and 8x200GE Ethernet speeds
Upgrades for ACC and Passive Copper DAC Support	
942-2130	UPG-ARESONE-1600GE-DAC, RETURN TO FACTORY upgrade for ARESONE 1600GE Integrated Heatsink (IHS) chassis (941-0130) to support ACC and passive DAC copper cables for 212.5Gb/s electrical lane speeds (942-2130)
942-2131	UPG-ARESONE-1600GE-DAC, RETURN TO FACTORY upgrade for ARESONE 1600GE Riding Heatsink (RHS) chassis (941-0131) to support ACC and passive DAC copper cables for 212.5Gb/s electrical lane speeds (942-2131)
IxNetwork Software Options	
930-2200	IxNetwork, node-locked perpetual license, All Inclusive package for AresONE. Supports all IxNetwork software features with exclusion; Excludes: 930-2207 IxNetwork Encryption test package for AresONE, 930-2208 IxNetwork RoCEv2 Lossless Ethernet Test Package for AresONE, 930-3461 IxNetwork AppLibrary Slot Bundle, Layer 4-7 Performance Test Application
930-2201	Ixia IxNetwork, node-locked perpetual license, Basic package for AresONE. INCLUDES: IxNetwork Base, RFC2544/2889/3918 QuickTest
930-2202	Ixia IxNetwork, node-locked perpetual license, Routing, Switching and Carrier Ethernet package for AresONE; INCLUDES: Routing, Switching and Carrier Ethernet Protocols; REQUIRES: 930-2201 IxNetwork Basic package for AresONE.
930-2203	Ixia IxNetwork node-locked perpetual license, MPLS and VPN package for AresONE; INCLUDES: Routing, MPLS and VPN Protocols; REQUIRES: 930-2201 IxNetwork Basic package for AresONE.
930-2204	Ixia IxNetwork node-locked perpetual license, SDN package for AresONE; INCLUDES: Routing and SDN Protocols; REQUIRES: 930-2201 IxNetwork Basic package for AresONE
930-2205	Ixia IxNetwork node-locked perpetual license, Data Center package for AresONE; INCLUDES: Routing, Data Center Overlay and Data Center Ethernet Protocols; REQUIRES: 930-2201 IxNetwork Basic package for AresONE.
930-2206	Ixia IxNetwork node-locked perpetual license, Broadband Access and Authentication package for AresONE; INCLUDES: Broadband Access and Authentication Protocols; REQUIRES: 930-2201 IxNetwork Basic package for AresONE
930-2207	IXIA IxNetwork, node-locked perpetual license, Encryption Test package for AresONE (930-2207); INCLUDES: MACsec Emulation; REQUIRES: 930-2201 IxNetwork Basic package for AresONE
KAI Data Center Builder	
950-2001	KAI Data Center Builder Start bundle - Collective Benchmarks application, uncounted license; Data Flow Emulation plugin, 16 universal transport endpoints. (12-Months Floating Worldwide License, Keysight software support subscription)
950-2002	KAI Data Center Builder Workloads bundle - Collective Benchmarks and Workload Emulation applications, uncounted license; Data Flow Emulation plugin, 16 universal transport endpoints. (12-Months Floating Worldwide License, Keysight software support subscription)
950-2101	KAI Data Center Builder - Collective Benchmarks application, uncounted license. (12-Months Floating Worldwide License, Keysight software support subscription)
950-2102	KAI Data Center Builder - Workload Emulation application, uncounted license. (12-Months Floating Worldwide License, Keysight software support subscription)
950-2201	KAI Data Center Builder - Data Flow Emulation plugin, 16 universal transport endpoints. (12-Months Floating Worldwide License, Keysight software support subscription)
950-2204	KAI Data Center Builder - Data Flow Emulation plugin, 8 Keysight hardware ports with unrestricted transport endpoints. (12-Months Floating Worldwide License, Keysight software support subscription)

More Information

See more information about the products at:

<https://www.keysight.com/in/en/products/ethernet-traffic-emulation/hardware-accelerators-for-protocol-and-load-test/aresone.html>



Keysight enables innovators to push the boundaries of engineering by quickly solving design, emulation, and test challenges to create the best product experiences. Start your innovation journey at www.keysight.com.

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